

```
//diagram ACTIONS
assign ADC_BUS = (CurrState_Sreg0 == `S2) ? ADC_in : 8'bz;
```

Module: ADC_control

DAVe

clk

reset

ADC_in[7:0]

ADC_INT

select

ADC_BUS[7:0]

ADC_out[11:0]

reset_DAVe

ADC_CS

ADC_HBEN

ADC_WR

ADC_RD

Sreg0

